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# Multicast cell scheduling protocol - US Patent 6856622

(F) At time T, **ingress switch port** 102A transmits the **cell** (F1) into the **crossbar** subsystem 104 and the **egress switch** ports 102B transmit their reverse ...

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# Short and long term fair shuffling for crossbar switch arbiter ...

The **cell** then flows through the **crossbar** from the source **port** 102-I to the ..... Equally, in\_free i is set if at least one of the **fanout** of **multicast** req\_i ...

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A copy of the **cell** arrives at each **egress switch port** 102 B that provided a reverse routing ..... To schedule a **multicast request** for an **ingress port** 102 A, ...

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#### Method, system, and storage medium for delay optimization for ...

Mar 18, 2008 ... There are a number of pairs of **ingress** line cards 104 and **egress** line cards 106 equal to the number of ports on the **data crossbar switch** 108 ...

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**switch** capacity of **multicast** for unicast **cells**. This scheme ..... These two kinds of traffic are all uniform in destination **port**. distribution. ...

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the data slice chips on a port (smart end) and a crossbar slice. chip (dumb end). .... the egress port. Overall the switch emulates a ClOQ switch ...

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# (WO/2005/048500) NONBLOCKING AND DETERMINISTIC MULTICAST PACKET ...

IN is a diagram of an exemplary four by four **port switch** fabric with input and output **multicast** queues and a speedup of two in the **crossbar** based ...

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Fourth, arbitrary fan- out multicast packets are not scheduled in nonblocking ... 1A is a diagram of an exemplary four by four port switch fabric with input ...

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### Scalable electronic packet switches - Selected Areas in ...

(receive) a **ce**ll to (from) the fabric. In turn, the **switch** fabric consists of three functional ele-. ments.. 1) the input **port**, which receives **data** units ...

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number of **crossbar** chips, **port** processor chips, and **data** slicing chips, ...... **Fanout** splitting, on the other hand, means that a **multicast** packet may be ...

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ate output **port**. Forwarding packets is considered part of the **data** plane of a. router. ..... high-bandwidth **cross-bar switch** rather than a bus. ...

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**switch** architectures that can scale with increasing rates and **port** counts. ..... while attempting to keep the **ingress** and **egress** either memoryless or with ...

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Since each input **port** can **switch** up to m **cells** in a time slot, ..... make it impossible to satisfy future demands between certain **ingress-egress** pairs. ...

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free space optical **crossbar switch** has been reported [62]. ..... nection **request** from an idle input **port** to some set of idle. output ports. ...

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each input or output **port** of the **switch** carries a (poten- ..... ingress to egress node. The establishment of this hop. sequence is performed during OPP ...

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two most common implementations of a **cross-bar switch** are the directive ...... source ring-and-star homed node but more importantly also the input **port** of ...

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1 Crossbar 2 Dr 2 Draht (2 Wire) 2/e Second Edition 2ASTP No. ...... of Withdrawal (ETSI) DP Data Partitioning DP Data Port DP Data Processing DP Dataport ...

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bridge-to-bridge station: A ship station operating in the **port** operations service ..... **crossbar switch**: A **switch** that has a plurality of vertical paths, ...

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## Telecommunications Glossary 2000

**crossbar switch**: A **switch** that has a plurality of vertical paths, a plurality of ...... the hosts of **data** sources, and, specifically, a communication **port**. ...

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switch port for every server, nor to purchase Fibre Channel host bus adapters ...... in the ingress direction, the egress direction, or both directions, ...

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nPX8005는 Crossbar 스위치 패브릭이고, 64/80B cell, 1024 VOQ(128 port, 8class), ...... Switch Matrix는 Ingress/

Egress Data/Control Buffers 와 Central Shared ...

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## Multicast cell scheduling protocol - Patent Review 6856622

A copy of the **cell** arrives at each **egress switch port** 102B that provided a ..... To schedule a **multicast request** for an **ingress port** 102A, the X-SCH chip ...

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Packets of **data** arrive at input **port**(s) of **ingress** means and are routed as **cells** across the **cross-bar** to a predetermined **egress** means which reassembles the ...

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Originating in the United States with "Port Folio" (1801-27, ...... The Rolling Stones made history with the first major multicast concert on the MBone. ...

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... darning DARPA Darwinism dashboard **data** database dative Datsun Daugherty ..... popularism populism Porifera **port's** porter porterhouse porters porticoes ...

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... cross crossable crossarm **crossbar** crossbarred crossbarring **crossbars** ..... porringer porringers porringer's **port** portabilities portability portable ...

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